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Design Case: 11 kW Totem-Pole PFC Boost Inductor for an 800 V OBC

From a failed first build to a passing DVP — with the numbers and the lesson

Item	Detail
Document	Anonymized design case — 11 kW totem-pole PFC boost inductor (800 V OBC)
Confidentiality	Customer identity withheld; parameters anonymized and rounded
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Revision / Date	EN-1.0 / 2026-07-16
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A buyer-private case: figures are anonymized/rounded engineering values, representative of the real project.

Summary & key takeaways

Bottom line

An 11 kW totem-pole PFC boost inductor was first sized from initial inductance — and collapsed to 21 μH at the 75 A peak (spec $\geq 30 \mu\text{H}$), failing DC bias. Rebuilt with stacked cores and turns cut from 30 to 21, it measured 31 μH at 75 A, 52 K temperature rise and 4.9 m Ω DCR — passing the full DVP. The one-line lesson: for powder-core inductors, always size turns from the biased inductance, never the initial value.

Key takeaways:

- **Scenario:** 800 V platform OBC, single-phase totem-pole PFC, 11 kW, $f_s=100 \text{ kHz}$, 50 A rms / 75 A peak.
- **Why V1 failed:** turns sized from initial AL; operating $H \approx 192 \text{ Oe}$ left only ~55 % of permeability — inductance collapsed.
- **The fix:** stack cores to double $A_e \rightarrow$ AL doubles $\rightarrow$ turns 30 $\rightarrow$ 21 $\rightarrow$ H drops to ~134 Oe $\rightarrow$ ~68 % permeability retained.
- **Winding:** edge-wound flat wire (5 $\times$ 1.6 mm), 21 turns, 4.9 m Ω DCR — about 30 % lower than round wire in the same window.
- **Result:** 31 μH at 75 A, 52 K rise (55 K limit), hi-pot/PD pass — full DVP passed.

Anonymization Customer identity withheld; parameters anonymized and rounded, but magnitudes and engineering logic match the real project — which is the point: a real design-fail-fix sequence you can learn from.

1. The requirement

Parameter	Requirement (anonymized/rounded)
Application	800 V platform OBC · single-phase totem-pole PFC
Power	11 kW (rated)
Switching frequency	100 kHz (SiC)
Current	50 A rms · 75 A peak
Inductance	$\geq 42 \mu\text{H}$ at 0 A; $\geq 30 \mu\text{H}$ at 75 A peak
DCR	$\leq 5.5 \text{ m}\Omega$
Temp rise	$\leq 55 \text{ K}$ (50 A rms, natural + duct airflow)
Insulation	800 V reinforced; hi-pot + PD
Environment	toward automotive Grade 1 (-40...+125 °C)

Table 1 — Customer requirement (anonymized)

The central tension is obvious: $\geq 30 \mu\text{H}$ still required at the 75 A peak — a DC-bias-dominated design where material and turns decide the outcome.

2. The first build: why it failed

V1 chose an Fe-Si powder core (high B_{sat} , low cost), single core, sized from the initial $AL \approx 51 \text{ nH/T}^2$: $46 \text{ } \mu\text{H}$ wanted, $N = 30$ turns. Perfect on paper — and at 75 A the sample measured only $\sim 21 \text{ } \mu\text{H}$, thirty percent short of the $30 \text{ } \mu\text{H}$ spec.

The root cause was treating initial inductance as operating inductance. One field-strength calculation shows it:

Operating field: $H = 0.4\pi \cdot N \cdot I / l_e = 0.4\pi \times 30 \times 75 / 14.7 \approx 192 \text{ Oe}$

Fe-Si μ_r - H curve: $\sim 55\%$ permeability left at 192 Oe

Actual inductance: $L \approx 46 \text{ } \mu\text{H} \times 0.55 \approx 25 \text{ } \mu\text{H}$ (measured $\sim 21 \text{ } \mu\text{H}$ with tolerance) → FAIL

Lesson A powder core's DC-bias curve is not 'reference information' — it is a design input. Turns must be derived from the biased inductance; skipping that step cost a full respin.

3. The fix: stacked cores + fewer turns

To hold inductance at 75 A there were two levers: switch to a more bias-tolerant material (High Flux, at a cost), or lower the operating H . We chose the latter — stacking two cores:

- **Ae doubles** → **AL doubles**: same inductance target with turns cut from 30 to 21.
- **H falls with N**: $H = 0.4\pi \times 21 \times 75 / 14.7 \approx 134 \text{ Oe}$ → $\sim 68\%$ permeability retained.
- **Inductance passes**: $L(75 \text{ A}) \approx 45 \text{ } \mu\text{H} \times 0.68 \approx 31 \text{ } \mu\text{H} \geq 30 \text{ } \mu\text{H}$ ✓

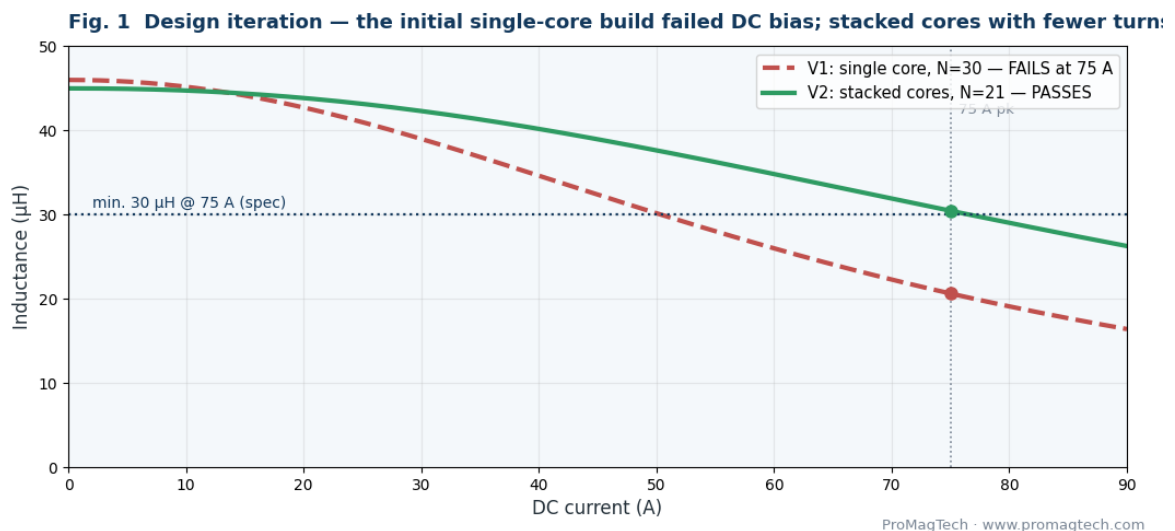


Fig. 1 — Iteration: V1 single core collapsed to $\sim 21 \text{ } \mu\text{H}$ at 75 A ; V2 stacked cores + fewer turns passed at $31 \text{ } \mu\text{H}$.

The winding was optimized in the same pass: edge-wound flat wire ($5 \times 1.6 \text{ mm}$), 21 turns on the stacked toroid, measured DCR $4.9 \text{ m}\Omega$ (spec $\leq 5.5 \text{ m}\Omega$). Round wire in the same window would run roughly 30 % higher — flat wire's copper fill is a real thermal advantage at this current.

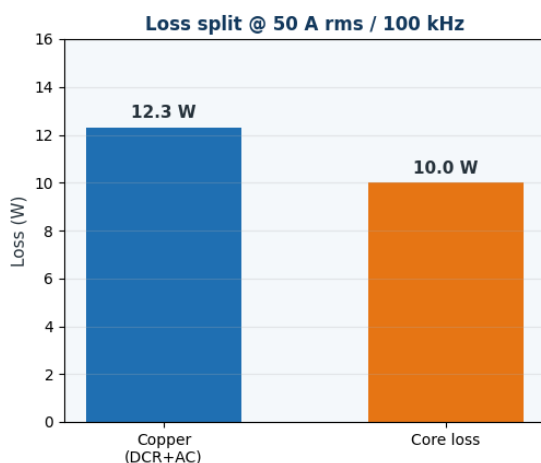
4. Loss & temperature: do the numbers add up

Loss accounting at 50 A rms / 100 kHz (rounded):

Copper: $P(\text{Cu}) = I_{\text{rms}}^2 \times \text{DCR} = 50^2 \times 4.9 \text{ m}\Omega \approx 12.3 \text{ W}$

Core: $\Delta B = L \cdot \Delta I / (N \cdot Ae) \approx 0.047 \text{ T pk-pk}$ → from loss curve $\approx 10 \text{ W}$

Total: $\approx 22 \text{ W}$ → measured rise 52 K (55 K limit, natural + duct) ✓

Fig. 2 Validated result of the final build (V2)

Sample DVP results (anonymized, rounded)

Item	Measured	Spec	Result
L @ 0 A	46 μ H	$\geq 42 \mu$ H	PASS
L @ 75 A pk	31 μ H	$\geq 30 \mu$ H	PASS
DCR	4.9 m Ω	$\leq 5.5 \text{ m}\Omega$	PASS
Temp rise @ 50 A	52 K	$\leq 55 \text{ K}$	PASS
Hi-pot / PD	pass	pass	PASS

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Fig. 2 — Final-build loss split and sample DVP results (anonymized/rounded).

5. Full DVP results

Item	Measured	Spec	Result
Inductance @ 0 A	46 μ H	$\geq 42 \mu$ H	PASS
Inductance @ 75 A pk	31 μ H	$\geq 30 \mu$ H	PASS
DCR	4.9 m Ω	$\leq 5.5 \text{ m}\Omega$	PASS
Temp rise @ 50 A rms	52 K	$\leq 55 \text{ K}$	PASS
Hi-pot	pass	per 800 V platform	PASS
Partial discharge (PD)	pass	per reinforced insulation	PASS
Batch consistency (pilot)	$\pm 4 \%$	$< \pm 7 \%$	PASS

Table 2 — Sample DVP results (anonymized/rounded)

6. Three lessons to take away

- **1. Derive turns from biased inductance:** compute operating H $\rightarrow$ read μ $\rightarrow$ correct AL $\rightarrow$ then set N. V1 skipped exactly this.
- **2. Core stacking is an under-used lever:** doubling Ae cuts turns, pressing H and copper loss down together — often cheaper than upgrading material.
- **3. Flat wire is thermal insurance at high current:** ~30 % lower DCR buys direct temperature margin; default to flat wire for 800 V high-current PFC.

7. Frequently asked questions (FAQ)

Q Why not just switch to High Flux?

A Possible, but cost rises noticeably. Stacking cores and cutting turns pushed the operating H back into Fe-Si's usable range at lower cost. When space forbids stacking, High Flux becomes the first choice.

Q Are the numbers real?



A They are anonymized, rounded engineering values consistent in magnitude and logic with the real project. Any specific project should be confirmed by sample test to your own spec.

Q Does this method scale to other power levels?

A Yes. Compute H, read the curve, size turns from biased inductance — the procedure is universal for powder-core inductors; 11 kW or 60 kW only changes the numbers.

Q Can ProMagTech build the same kind of sample?

A Yes. Send power/topology/current/inductance/temperature limit and we run this exact procedure — selection, sampling, and a DVP report delivered with the parts.

8. Further reading

This case belongs to the ProMagTech 800 V Power Magnetics Selection Reference; the methodology behind it:

- [800 V Power Magnetics Selection Reference \(the pillar map\)](#)
- [OBC PFC boost inductor selection \(methodology\)](#)
- [Choosing a powder core: Fe-Si / Sendust / High Flux / MPP \(materials\)](#)
- [AEC-Q200 & automotive reliability \(validation framework\)](#)
- [Flat wire vs round wire inductors \(winding\)](#)
- [Anonymized case: 120 V/40 A/66 kHz flat-wire inductor \(sister case\)](#)

9. About ProMagTech

SHENZHEN PROMAGTECH CO.,LTD (founded 2013, Shenzhen) designs and manufactures flat-wire inductors, planar transformers, common-mode chokes and PFC inductors for 800 V EV OBC and DC charging, AI-datacenter power, energy-storage PCS, solar inverters and industrial power.

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Last updated: 2026-07-16 · Revision EN-1.0

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