PMC-EQ32 High-Current Flat-Wire Inductor

Product Design Reference | PMT-PR-2026-1112-02 | Rev A/0 | 2026-11-12

This reference reproduces published product information and its stated evidence status. Final project specifications require an approved drawing and sample acceptance record.

Application and Validation

This page is a model-level engineering reference, not a fixed off-the-shelf catalog promise. The public evidence is limited to anonymized sample-stage information: EQ32-class Fe-Si core review, FeSi60u DC bias behavior, compact mechanical-envelope constraints, DCR target review, insulation boundary review and sample validation scope.

Final inductance, DCR, saturation current, temperature rise and insulation details must be confirmed against the approved sample record and the buyer's actual converter conditions.

Two-page PDF for reviewing FeSi60u permeability behavior under DC bias. Use it as material-level support during EQ32-class inductor feasibility review.

Public sample-stage case covering EQ32 Fe-Si core behavior, DC bias roll-off, compact mechanical envelope, insulation and sample-stage quality checks.

For a project-specific PMC-EQ32 review, provide converter topology, input and output voltage, operating frequency, RMS and peak current, ripple current, inductance target, DCR target, thermal limit, mounting envelope, insulation requirement and expected validation documents.

Review the sample identification, test method and current-dependent inductance in each report. These reports apply to the tested samples.

Project Review and Contact

Send voltage, topology, frequency, RMS and peak current, loaded inductance or turns ratio, thermal boundary, insulation and available mounting dimensions. Sample fees and delivery schedule are confirmed in the project quotation.

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Product page: https://www.promagtech.com/products/pmc-eq32-high-current-flat-wire-inductor.html

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